ZEPHYR BLOCK DIAGRAM

COMPUTER-BASED EDUCATION RESEARCH LABORATORY
UNIVERSITY OF ILLINOIS

BB-028-4

TOLERANCES	DRAWN BY	SCALE
FRACTIONAL ±	WILSON	
3 PLACE DECIMALS ±	APPROVED BY Lippold Haken	DATE 6-15-88
2 PLACE DECIMALS ±		

REVISED	
3	7-26-89
2	10-26-88
1	8-22-88

MICROCODE FIELDS

	MMAR TRUE	MMAR FALSE	SELECT	QT	QRS	PT	PRS	JMP	COND	BZD2	WAIT	WRS	AZBSA	BZBSA	BZD1	BZD0	AZD	RMFLD	WMFLD	ENABLE	SCMAD	SPC	MEM-CHK	BM-WRITE	FFPU	PCI
BITS	79 78 77 76	75 74 73 72	71 70 69 68	67 66 65 64	63 62 61 60	59 58 57 56	55 54 53 52	51 50 49 48	47 46 45 44	43 42 41 40	39 38 37 36	35 34 33 32	31 30 29 28	27 26 25 24	23 22 21 20	19 18 17 16	15 14 13 12	11 10 09 08	07 06 05 04	03 02 01 00
	SENT TO TRUE MICROCODE ADDRESS MUX	FALSE MICROCODE ADDRESS	ALU FCN, MASK, BARREL SHIFT, X↔T, PACK, BM CONTROL	A, B, X18, X60, ENONE, ZEROS, jk	ifld, ifld, kfld, mfld	B, X18, C X60, ENONE, ZEROS, K	ifld, ifld, kfld, mfld	TRUE ADDRESS MUX CONTROL	MICROCODE CONDITIONAL JUMP SELECT	SEE BZD1 BZD0	STEP TIME = (WAIT+2)·10ns	ifld, jfld, kfld, mfld	ALU BARREL SHIFT, BBOARD DBOARD	ALU, CM, BM, Z BUS MUX SELECT	CM, CMAC, CMRAFL, BMRAFL, BMCTL, MON	A, X, C	B,	Ø – 7	Ø – 7	QREGØ, BMSTUFF, XJFF, BMLR, BMAR, ENPRC, MAR, BMCTL, ETC.	CMAC PC MAR	CONDITIONAL LOAD X OR B, LOAD18 OR 20, ADD PCI INCREMENT	MEM ERR FF	ENAB	R/W	FLOATING OPERATIONS, PACK, UNPACK, POPULATION COUNTS, BM READS	PARCEL INCR.