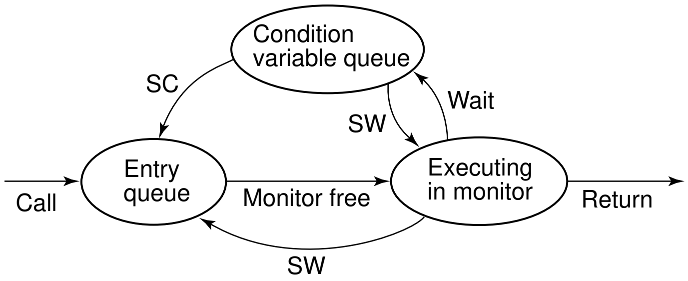

Figure 5.1 State diagram for synchronization in monitors.